# WriteNow! Series

# Single and Parallel In-System Programmers

# Additional Document for User's Manual

# Programming Algorithm for Renesas

# 78K family

AN00010200EN - Rev. 1.00



# Introduction

This Application Note provides WriteNow! programming commands' specifications for Renesas 78K family.

WriteNow! Technology allows to program up to 8 devices at once, by reducing, in a drastical way, programming times, costs and system complexity. Algocraft provides support of Renesas 78K family through all of its WriteNow! Series, single and parallel In-System Programmers. Our four WN programmers are: WN-PRG01A (programs 1 device by one), WN-PRG02A (programs 2 devices in parallel), WN-PRG04A (programs 4 devices in parallel), and WN-PRG08A (programs 8 devices in parallel). In addition, we provide external Demultiplexer modules as accessories for the multiple-sites WriteNow! models, able to extend the programming channels up to 32 in a sequential way (8+8+8+8).

Read the WriteNow! User's Manual included in the software before to use the WriteNow! Programmer.

# **78K Family Overview**

The extensive 78K series lineup provides the ideal product for almost every application. For a compact, low-power MCU, choose one of the products in the 8-bit 78K0 and 78K0S Series, and for a controller that offers the performance of a 32-bit MCU but consumes the power of an 8-bit one, look no further than the 16-bit 78K0R.

# **Communication Protocols**

The Programmer supports two types of protocols: CSI and UART.

#### **CSI protocol:**





In the CSI protocol, the Programmer is always the master of the communication. For this communication, the SCK, SO, and SI pins are used. Data must be handled in 8-bit units, with the MSB first. In order to view the connections for your selected target device, select Debug > Show ISP Connections from the WriteNow! GUI interface as below:

		ISP A Connect	or ISP B Connector
C B A	16 15 14 1 Site 4	3         12         11         10         9         8         7         6           3         12         11         10         9         8         7         6           Site 3         Site 3         Site 2	5         4         3         2         1           Site 1         Site 5         Site 5         Site 5
Color	Signal Name	Target Signal Name	Target Signal Description
	SxL01	FLMD	FLMD
	SxL02	SCK	Serial Clock
	SxL03	SO	Serial Data Output
	SxL04	CLKOUT	
	SxL05	NC	
	SxL06	SI	Serial Data Input
	SxL07	RESET	RESET
	SxPPS	VDD	Target VDD (Programmer can provide a power supply for the target system)
	SxGND	GND	GND
	SxRLY	RLY	Relay output

#### UART protocol (78K0, flow-A type):



About UART protocol, the Programmer is always the master of the communication. For this communication, the TxD and RxD pins are used. Data must be handled in 8bit units, with the LSB first. In order to view the connections for your selected target device, select Debug > Show ISP Connections from the WriteNow! GUI interface as below:



		ISP A Connect	or ISP B Connector
C B A	16 15 14 1 Site 4	3 12 11 10 9 8 7 6 Site 3 Site 2	5         4         3         2         1           2         Site 1         Site 3         2         1
Color	Signal Name	Target Signal Name	Target Signal Description
	SxL01	FLMD	FLMD
	SxL02	TXD	TXD
	SxL03	NC	
	SxL04	CLKOUT	
	SxL05	NC	
	SxL06	RXD	RXD
	SxL07	RESET	RESET
	SxPPS	VDD	Target VDD (Programmer can provide a power supply for the target system)
	SxGND	GND	GND

#### UART protocol (78K0S, Single-wire, flow-B type):



This communication System is exclusively for the 78K0S/Kx1+. The DGDATA pin is used for communication. Data must be handled in 8-bit units, with the LSB first, baudrate at 115200 bps. In order to view the connections for your selected target device, select Debug > Show ISP Connections from the WriteNow! GUI interface as below:



WN-PRG	i08A ISP Conr	nections for Nec UPD	78F9201 [upd7001	]	×
		ISP A Connect	or	ISP B Connector	
C B A					
	16 15 14 1 Site 4	3 12 11 10 9 8 7 6 Site 3 Site 2	5 4 3 2 1 2 Site 1	16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 Site 8 Site 7 Site 6 Site 5	
Color	Signal Name	Target Signal Name	Target Signal Des	cription	
	SxL01	DGCLK	DGCLK		
	SxL02	NC			
	SxL03	NC			
	SxL04	CLKOUT			
	SxL05	DGDATA	DGDATA		
	SxL06	NC			
	SxL07	RESET	RESET		
	SxPPS	VDD	Target VDD (Prog	rammer can provide a power supply for the target system)	
	SxGND	GND	GND		
	SxRLY	RLY	Relay output		
				ОК	

#### UART protocol (78K0R, Single-wire, flow-C type):



The TOOL0 pin is used for communication. Data must be handled in 8-bit units, with the LSB first. In order to view the connections for your selected target device, select Debug > Show ISP Connections from the WriteNow! GUI interface as below:



		ISP A Connect	tor ISP B Connector	
C B A	16 15 14 1 Site 4	3 12 11 10 9 8 7 6 Site 3 Site 2	c c c c c c c c c c c c c c c c c c c	2 1 5
Color	Signal Name	Target Signal Name	Target Signal Description	
	SxL01	FLMD0	FLMD0	
	SxL02	NC		
	SxL03	NC		
	SxL04	CLKOUT		
	SxL05	TOOL0	TOOL0	
	SxL06	NC		
	SxL07	RESET	RESET	
	SxPPS	VDD	Target VDD (Programmer can provide a power supply for the target syste	em)
_	SxGND	GND	GND	

### **Data Formats (Flash Process Flow)**

The Renesas 78K family uses different programming flow based on the following table:

Signature	Flash	Device	Flash Process Flow
(Byte 2-3)	Macro		
7F-01	CZ6HSF	78K0	Flow-A
7F-04	MF2	78K0	Flow-A
7F-04	MF2	78K0R	Flow-C
EF-04	MF2	78K0R(2 <sup>nd</sup> )	Flow-C
None	CZ6HSF	78K0S	Flow-B
DF-04	MF2	78K0(2 <sup>nd</sup> )	Flow-D

Each Flash Process Flow differs from each other in command/communication data format and in processing. More information can be found on the Renesas "Single Voltage Flash MCU Standard Specifications for Serial Writer" Manual (Not disclosed to users).

# WriteNow! Device Settings (PR5 file)

WriteNow! Programmer uses the settings of the Renesas Parameter File (PR5 file) used by the Renesas PG-FP5 Flash Memory Programmer. The PR5 file contains important information about communication process such as Wait's Parameters for send/receive command and data, Wait's Parameters for blank Check, Erase, Program, Verify and read operations (delay, timeout, retry number), memory map address, signature string configuration, etc.



<u>F</u> ile	<u>P</u> roject <u>D</u> ebug <u>T</u> ools <u>H</u> elp
*	📑 🖿 🖏 🖹 🕼   🎞 💽   🔚 🔁   🕐
10	///////////////////////////////////////
11	<pre>// Sets device-specific parameters</pre>
12	// These commands must NOT be modified by user
13	1//////////////////////////////////////
14	#dev -o begin
15	#dev -o set -p ver -v [h10000]
16	#dev -o set -p nprc -v [h01 h00 h00]
17	#dev -o set -p PRC0_10 -v [h10 h7F h4 h7C h7F h7F h80 hC4 h37 h38 h46 hB0 hB5 h31 h31 h20 h20]
18	#dev -o set -p PRC0_11 -v [0 0]
19	#dev -o set -p PRC0_12 -v [200 200]
20	#dev -o set -p PRC0_13 -v [0 0]
21	#dev -o set -p PRC0_14 -v [0 h0]
22	#dev -o set -p PRC0_15 -v [0 h1]
23	#dev -o set -p Commands0_0 -v [1500 1500 1605 0 0 3302 0 0 1605]
24	#dev -o set -p Commands0_1 -v [256 1100 1300 1300 1500 1500 0 0 0 1800 1800 0 0 1605]
25	#dev -o set -p Commands0_2 -v [0 9202 1605 1484 h10]
26	#dev -o set -p Commands0_3 -v [0 3134 1606 3134 h33 0 0 0 h0 0 0 0 h0]
27	#dev -o set -p Commands0_4 -v [0 1453 1605 1453 h100 2013 2566 3224 h50 h0 2601]
28	#dev -o set -p Commands0_5 -v [0 2702 1605 2702 h1000 0 1401]
29	#dev -0 set -p Commands0_6 -v [0 0 5883 1606 5883 h100 7803 1606 7803 h100 1901]
30	#dev -0 set -p Commands0_7 -V (0 0 1201 1103)
22	#dev -o set -p Commandso_9 -v [0 0 0]
22	#dev -0 set -p Commands0_B -v [2501 3602]
24	#dev =0 set =p Commands0_C =v [2501 2003 h0]
35	#dev -0 set -p Commands0_b -v [0 0 0 10 0 0 0 10 0 0 0 10]
36	Her - o set -p CommandsO_E -v [0300 0000 mi0]
37	#dev -0 set -p flow -v [b41 b04 b7f b00 b44 b37 b38 b46 b30 b35 b31 b31 b00 b00 b00 b00]
38	#dev -o set -p type -v [h01 h01 h01 h00 h02 h00 h02 h01 h02 h01 h01 h02 h01 h00 h00 h00]
39	idev -o end
55	jacr o cha

# WriteNow! Project Settings

All device-specific settings, target Parameters and connection values are set by WriteNow! GUI interface (Project Creation Wizard). The Wizard will automatically fill all data with typical values for the selected target device.

arget I/O Target Power Supply Communication	
Target I/O	
Device I/O voltage (mV):	3300
Reset up time (us):	100
Reset down time (us):	100
Reset I/O drive mode:	pushpull 🔻
Clock I/O drive mode:	hiz 🗸
Clock I/O frequency (Hz):	16000000

The **"Device I/O voltage"** setting specifies the voltage of the ISP lines. You should check the target board schematics, or ask the board developer about this value. The allowed voltage also depends on the selected target device.

The "Reset up time (us)", "Reset down time (us)" and the "Reset I/O drive mode" settings grant you to configure the RESET pin for the establishing communication between the programmer and device.

The **"Clock I/O drive mode"** and **"Clock I/O frequency"** settings allow you to decide how the SxL04 ISP line is driven. This line can be used as an auxiliary ISP line



(to provide a clock to the target device), as a generic I/O line, or as a highimpedance output (no electrical driving). We suggest leaving this line floating (HiZ) when not used, in order to decrease the electrical noise on other ISP lines.

The Wizard will automatically fill the commands in the project file (wnp) as below:

```
#conf -o begin
 // Target I/O settings
 // Target I/O
 // Set Device I/O voltage (mV)
 #conf -o set -p vddio -v 3300
 // Set Reset up time (us)
 #conf -o set -p rstup time -v 100
 // Set Reset down time (us)
 #conf -o set -p rstdw_time -v 100
 // Set Reset I/O drive mode
 #conf -o set -p rstio_drive -v pushpull
 // Set Clock I/O drive mode
 #conf -o set -p clkio_drive -v hiz
 // Set Clock I/O frequency (Hz)
 #conf -o set -p clkio freq -v 16000000
Project Creation Wizard, Step 2/3
 Set the following values to configure the target's communication and other parameters.
                                                1 2 3
 Target I/O Target Power Supply Communication
  Target Power Supply
  Target power supply voltage (mV):
                                3300
  Power up time (ms):
                                100
  Power down time (ms):
                                100
 Cancel
                                       <-- Previous Next -->
```

Target Power Supply tab controls the programmable power supply line available for each ISP site.

The voltage of the programmable power supply line ("**Target power supply voltage**" setting) can be in the range 1700mV to 13000mV.

The "**Power up time**" and the "**Power down time**" settings specify the delay between the programmable power supply line turning on / off and subsequent operations.

The Wizard will automatically fill the commands in the project file (wnp) as below:



59	<pre>// Target Power Supply settings</pre>
60	// Target Power Supply
61	// Set Target power supply voltage (mV)
62	<pre>#conf -o set -p pps_voltage -v 3300</pre>
63	// Set Power up time (ms)
64	<pre>#conf -o set -p pps_uptime -v 100</pre>
65	// Set Power down time (ms)
66	#conf -o set -p pps dwtime -v 100

oject Creation Wizard, Step 2/3	s communication and other narameters
Target I/O Target Power Supply Communication	
Communication	
Communication protocol:	SIO-CH0 -
Programming entry pulse number:	8
Internal oscillator frequency (Hz):	8000000
CPU Bus frequency (Hz):	8000000
Communication frequency (Hz):	2500000 👻
Carral	
Cancel	< Previous Next>

Communication tab sets the parameters for the protocol. This windows can change from device to device based on programming process flow.

The "**Communication protocol**" specify the type of protocol, for Renesas 78K family the protocol could be UART or SIO(SPI) protocol.

The "**Programming entry pulse number**" is the number of pulses on FLMD0 pin to set the correct "Microcontroller Operating Mode". By executing the flash firmware, the flash microcontroller reads the number of pulses stored in the builtin timer/counter to decide on the communication system. This settings can not be edited by user.

The "Internal/External Oscillator frequency" and "CPU bus frequency" specifies the values (in Hz) of the oscillator frequency and CPU bus frequency (not edited).

Finally the "**Communication frequency**" or the "**Baudrate**" set the speed of the communication. Maximum bitrate speed achievable is affected by wiring environment used; refer to Algocraft's WP00010101EN Understanding ISP Wiring white paper for further more information.

The Wizard will automatically fill the commands in the project file (wnp) as below:



```
67 // Communication settings
68 // Communication
9 // Set Communication protocol
70 #conf -o set -p protocol -v SIO-CH0
71 // Set Programming entry pulse number
72 #conf -o set -p entry_pulse_num -v 8
73 // Set Internal oscillator frequency (Hz)
74 #conf -o set -p osc_freq -v 8000000
75 // Set CPU Bus frequency (Hz)
76 #conf -o set -p bus_freq -v 8000000
77 // Set Communication frequency (Hz)
78 #conf -o set -p bitrate -v 2500000
```

In the next step you select which programming operation to perform on the target.

Project Creation Wizard, Step 3/3
Select the programming steps to perform.
C Enable Power Supply output
Check Communication with device
V Unprotect and Erase all memories
V Blank Check Program memory
V Program Program memory
Verify Program memory (readout method)
Verify Program memory (checksum method)
Cancel Cancel Finish

Enable the "**Enable Power Supply output**" option in order to power the board by the WriteNow! SxPPS power line.

The "**Check Communication with device**" option establishes a communication between WriteNow! Programmer and target device. This option should be used during the project setup in order to be sure that a good wiring and electrical parameters has been reached.

The "**Unprotect and Erase all memories**" option removes the device protection and erase the entire memory.

The "Blank Check Program memory" option checks if the device memory is blank.

The "Program Program memory" option program the device memory.

The "**Verify Program memory (readout method)**" command is used to compare the data, in a specified address range, sent from the programmer with the data read from the device (read level) to confirm whether they match.

The "**Verify Program memory (checkum method)**" command is used to verify the device memory using the checksum method.



```
83 // Power Supply on

84 #prog -o cmd -c pps -v on

85 // Target Connection

86 #prog -o cmd -c connect

87 // Unprotect and Erase all memories

88 #prog -o cmd -c erase -m all

89 // Blank Check memory

90 #prog -o cmd -c blankcheck -m flash -t h0000 -1 h30000

91 // Programs memory

92 #prog -o cmd -c program -m flash -s h0000 -t h0000 -1 h30000

93 // Verifies memory (checksum method)

94 #prog -o cmd -c verify -v chks -m flash -s h0000 -t h0000 -1 h30000
```

Note: The "**Read Program memory**" command is not available on the Renesas 78K family.

#### 78K0R/Fx3 Data Flash Programming

Data Flash of this family cannot be programmed directly.

Following the Renesas specification, the programming of the Data Flash needs to follow these steps:

- Program the Microcontroller with a special firmware (provide by Renesas)
- Execute the firmware in normal mode
- In this mode, the firmware will copy the data from 0x2000 address to Data Flash area.
- Program the Microcontroller with a second project with the user firmware.

In WriteNow! Project generator GUI follow these steps:

#### Step1:

A special .wni file must be created. At first you have to add the right .s19 file selected from \drivers\uk\78F18XXDF list. This file is the Renesas firmware.

Then you have to add the binary file of the DataFlash. The data must be loaded from 0x2000 address, according the following picture:





The dataflash content which is stored from address 0x02000 onwards is the pure binary data without ID tags, addresses, and checksum. The Renesas firmware will copy this data one by one to the real dataflash.

Data Type	Parameters	Buffer Range	Add
FILE	78F1845DF.s19, SREC, 0000000-00000ABF	0000000-00000ABF	= 10
CONST	H22,h11,hab	00002000-00005FFF	Edit
			Remove
utput File Format: Filename:	WriteNow! Image		
	Fill unused locations with: HFF		

#### Step2: Project settings and programming steps

See the picture below:



Project Creation Wizard, Step 3/3
Select the programming steps to perform.
Enable Power Supply output
Check Communication with device
Unprotect and Erase all memories
Erase Program memory
Blank Check Program memory
Program Program memory
Verify Program memory (readout method)
Verify Program memory (checksum method)
Execute Dataflash programming code
Cancel C Previous Finish

It is important to enable the "**Execute DataFlash programming code**" command. This command executes the Microcontroller in normal mode and the firmware will copy the data from 0x2000 address to Data Flash.

# **Programming Times**

The following table shows programming times for selected Renesas 78K family devices. Programming times of your current System may therefore be different from the ones listed here below.

Device	Protocol / Speed	Memory Size	Operations	Times (s)
UPD78F0393	CSI @250000	32KB	E+BC+P+V	7 s
UPD78F0502	CSI @250000	24KB	E+BC+P+V	5,2 s
uPD78F1845	UART @ 2000000	256KB	E+BC+P+V (Checksum)	10,1 s

# References

- Algocraft WriteNow! Series User's manual
- Algocraft Understanding ISP Wiring White Paper
- Renesas Specification for Fx3 Dataflash Programming (Confidential)
- Renesas Single Voltage Flash MCU Standard Specifications for Serial Writer (Confidential)
- Renesas PG-FP4 System Specification (Confidential)
- Renesas device-specific User's Manual



# Conclusion

Algocraft WriteNow! Programmer's Series represents a professional Solution involved, above all, in the automotive field by many important customers. WriteNow! In System Programming solution has been consolidated for more than 5 years and it keeps on being strengthened thanks to many installations on various In Circuit Test machines, representing a competitive advantage compared to other solutions of the worldwide market.

We work daily to ensure and keep on affirming high standard. **Why?** The continuous improvement, the maintenance of the highest quality and programming standards by adding new devices, by trying to improve our performances, our programming times...are our goal!

**How?** We are able to develop new algorithms upon request by customers enlarging

our device list, we are capable to better our performances, we try to beat our programming times continuously, challenging at first ourselves!

